

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>				Docket Number 204552016500	Application Number Unassigned	
				Applicant	Yuichi SATO	
				Filing Date October 5, 1999	Group Art Unit Unassigned	

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U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO

OTHER DOCUMENTS*(including author, title, Date, Pertinent Pages, Etc.)*

Examiner Initials	Ref. No.	Title
LJL	1.	<u>"PRINCIPLES OF CMOS VLSI DESIGN,"</u> A Systems Perspective, Second Edition, Neil H.E. Weste and Kamran Eshraghian, Chapter 10, pp: 578-83

EXAMINER: (examiner)

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.